In the Specification:

Please delete paragraph **[01]** and replace it with the following amended paragraph:

[01] This application is related to, and claims benefit of and priority from, Provisional Application No. 60/274,433 filed March 9, 2001, titled "Digital Phase Locked Loop for Regenerating the Clock of an Embedded Signal", Provisional Application No. 60/263,792 filed January 24, 2001, titled "System and Method for Increased Data Capacity of a Digital Video Link", and Provisional Application No. 60/268,840 (Attorney Docket No.: 41821/JEJ/B600) filed February 14, 2001, titled "Digital Visual Interface with Audio", the complete subject matter of which is incorporated herein by reference in its entirety.

Please delete paragraph **[05]** and replace it with the following amended paragraph:

[05] Accordingly, there is a need for a system and method for using the blanking period of the video data to transmit audio data. There is a further need to embed audio data in the video data stream blanking period and then regenerate the clock of the embedded audio data using a phase locked loop for example.

Please delete paragraph **[15]** and replace it with the following amended paragraph:

[15] Fig. 2 illustrates a block diagram of a ~~DVI~~ Digital Video Interface "DVI" system adapted to send data over a high speed serial ("DVI") link in accordance with one embodiment of the present invention;

Please delete paragraph **[25]** and replace it with the following amended paragraph:

[25] Fig. 1 illustrates one method, generally designated 10, for regenerating a clock rate of an embedded signal transmitting audio data inserted into unused portions of the video steam. Audio data at a first clock ~~rated~~ <u>rate</u> is split and inserted into unused lines (blanking portions) of the video stream, forming the composite stream as illustrated by block 12.